

RESOURCE PERSONS

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Dr. Angsuman Sarkar, KGEC

Mr. Pritam Banerjee, Trident Techlabs



**FACULTY DEVELOPMENT
PROGRAMME ON
“ADVANCES IN
MICROELECTRONICS ,VLSI
DESIGN AND OPTOELECTRONICS
DEVICES WITH MIXED SIGNAL
ANALYSIS”**

**ORGANIZED BY
DEPARTMENT OF
ELECTRONICS AND
COMMUNICATION ENGINEERING
NARULA INSTITUTE OF TECHNOLOGY**

UNDER
TEQIP PHASE II,
SUB-COMPONENT 1.1
FROM 13/01/14 TO 25/01/14



**VENUE
NARULA INSTITUTE OF TECHNOLOGY
81, NILGUNJ ROAD AGARPARA
KOLKATA-700109
PHONE:033-2563-8888/7777**



FDP ON “ADVANCES IN MICROELECTRONICS, VLSI DESIGN AND OPTOELECTRONICS DEVICES WITH MIXED SIGNAL ANALYSIS”

13th January-25th January, 2014

OBJECTIVE

The FDP intends to provide opportunities to faculty members, research scholars to enrich their intellectual vitality and further their professional growth. The major focus of the programme is for the upgradation of teaching skill and research for faculty members in the field of microelectronics, VLSI, optoelectronics and signal processing . The Programme also aims to enhance the knowledge of participants for simulation with advanced software in the relevant field. These are also essential for inculcating learning values in students and guiding and monitoring their progress towards their career.

OUTCOME

Through the programme, participants can equip with new technological expansion to achieve the diverse, complex, and often difficult-to-develop analysis of their research work in the field of fabrication and designing of devices and signal processing in biomedical, data, image etc. Participants become more learner-centered educators while developing collegial relationships with other faculty.

ABOUT THE INSTITUTE

Narula Institute of Technology established in 2001 as an educational initiatives of JIS Group of academic institutions. The institute aims at providing high quality technical education to the budding engineers and to skill them to make a mark of excellence in Education, Business and Industry. This college has been aided by World Bank under TEQIP Scheme. The college is going to be an autonomous institution.

ABOUT THE DEPARTMENT

ECE Department has been started from the inception of the institute. Not only B.Tech course, M.Tech courses like ECE (Communication) and M.Tech ECE (Microelectronics & VLSI Design) has also started . There are different excellent laboratories in this department. AICTE funded two MODROBS projects have been completed successfully and one is running. IETE student Forum (ISF) is functioning actively in this department.

FDP ON "ADVANCES IN MICROELECTRONICS, VLSI DESIGN AND OPTOELECTRONICS DEVICES WITH MIXED SIGNAL ANALYSIS"

Registration Form

1. Name:
2. Email id:
3. Contact number:
4. Date of Birth:
5. Highest Degree:
6. Designation:
7. Institution/organization:
8. Gender: M/F
9. Food : veg/non-veg
10. Brief description of your teaching/ research/working area:
11. Purpose of attending the FDP:
12. Demand Draft No. with date:

.....
Signature of the applicant

.....
Signature of the head of the department / organization with seal

Details of Registration

Registration Fee:

Academic Person/Faculty : Rs.1,000/-

Research Scholar/PG Student : Rs.500/-

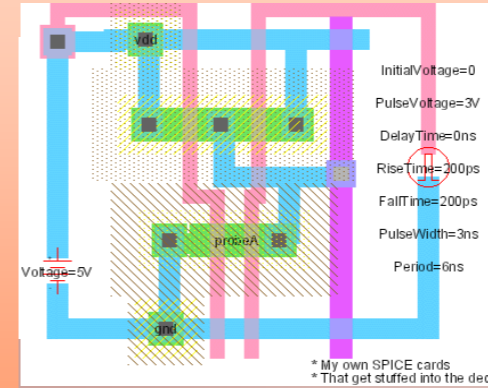
Payment should be done through demand draft only in favour of "M/S NARULA INSTITUTE OF TECHNOLOGY A/C STAFF DEVELOPMENT FUND" payable at Kolkata

Download the registration form from the website www.nit.ac.in and scanned copy of demand draft and filled registration form should send to the following email id: ecenitfdp2014@gmail.com

Registration will be on first come first basis, limited seats are available

Hard copy should reach within 8th January, 2014 to programme co-ordinator:

Mrs. Swati Banerjee
Programme Coordinator
FDP 2014, ECE Department
Narula Institute of Technology
81, Nilgunj Road
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CONTACT

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