



IEEE CASS KOLKATA TECHNICAL PAPER CONTEST, 2019

Date: 3rd May, 2019

Time: 1:00pm to 5:30pm

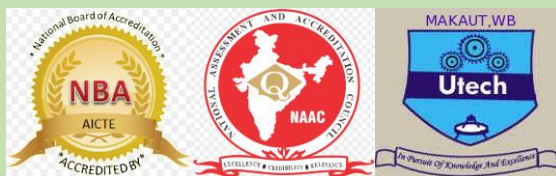
**Venue: Narula Institute of Technology,
Kolkata**

Jointly organized by

**IEEE CAS Kolkata Section & IEEE CAS
Student Branch Chapter
In association with**

**Electronics and Communication
Engineering Department**

**Narula Institute of Technology
(An Autonomous Institute)**



About NiT

Narula Institute of Technology, (NiT), is a leading autonomous AICTE approved NACC accredited Engineering & Management College is affiliated to MAKAUT, WB located at Agarpara near Kolkata under the aegis of JIS Group since 2001. It boasts of the prestigious NIRF ranking among 150 selected institutes of India. NiT is running eight under graduate, four post graduate, three numbers of Diploma level programs and is also engaged in research activity leading to Ph.D.

The institute has its own R& D cells and substantial repository of International and National Journal publications, E-books, Online Lectures.

The Institute received ten numbers R& D projects funded by UGC, BRNS and AICTE are running successfully till date. The Institute received World Bank assisted MHRD approved TEQIP (Phase-II) Grant for Institutions technical Education Quality Improvement. The Institute is eligible for receiving central assistance under UGC act.

Recently the Institute has secured the DST-FIST grant to upgrade the Research Facilities. The Institute has collaborations with Tech Mahindra, Reliance Jio Infocom Ltd, CII, NASSCOM, Aspire Disruptive Skill foundation, Oracle, INFOSYS, NIT Sikkim, IIT-KGP, AIT Bangkok, Jadavpur University, IEEE, IET and other organizations of repute.

About the ECE Department

The department offers premier professional technical teaching & training that keeps in pace with the latest developments in the field of Electronics and Communication Engineering. The Department has excellent infrastructure, highly experienced faculties and professionals to deliver best technical education in ECE domain with track towards Industry. The department organized International & National Educational Programs (i.e. IEEE sponsored Seminars, Workshops, Faculty Development Programs and one day educational Industrial tours) in regular fashion. The Department also organized Paper presentation, Poster presentation,

Technical Quiz contest, Technical Report Writing Contest & Technical Workshop cum contest in association with IIT, Bombay. Department has active IEEE CAS Student Branch Chapter and student chapter of IETE. The Dept. has more than 250 research publications in National & International Journals & Conferences. The Dept. is NBA accredited and awarded four on-going MODROB Projects, One RPS funded by AICTE, One Minor Project funded by UGC. The R&D laboratory of the Department has professional simulation software for research and training, like Tanner SPICE, CADENCE, Matlab, Visual TCAD, HFSS, SIMICA, SYNOPSIS, PROTIUS, Qualnet, Active HDL, MPLS, Altera & Xilinx ISE, TMS Kit with Code Composer Studio, ARM, PIC & DSP Development Board and Advanced FPGA kit, PCB fabrication laboratory etc.

About IEEE CAS Chapter

The IEEE Circuits and Systems Society is the leading organization that promotes the advancement of the theory, analysis, design, tools, and implementation of circuits and systems. The field spans their theoretical foundations, applications, and architectures, as well as circuits and systems implementation of algorithms for signal and information processing. The Society brings engineers, researchers, scientists and others involved in circuits and systems applications access to the industry's most essential technical information, networking opportunities, career development tools and many other exclusive benefits. Local members with similar technical interests engage in professional exchange through the Society's 10 regional chapters in the United States, Canada, Europe, the Middle East, Africa, Latin America, Asia, Australia and the Pacific.

About Technical Paper Contest

The technical paper contest will be done in two rounds.
1st round: The Juries of the Contest will decide the acceptable quality papers for power point presentations

2nd round: The selected candidates will appear for presentation before Juries of the contest. The Juries of

the contest will judge the paper presentations and will select three awardees (1st, 2nd and 3rd)

Cash prizes will be awarded along with memento to the successful contesters. All successful contesters will receive Certificates.

Call for Paper:

- + VLSI Circuits
- + Circuits for Electronic Communications
- + Circuits for Embedded systems applications
- + Electron device circuits
- + Circuits for sensing systems
- + Circuits for control and automation
- + Signal processing & Circuits
- + Image Processing & Circuits
- + Remote sensing & Circuits
- + AI & Robotics

Guideline for paper submission:

UG and PG students of both Engineering and Science Background are eligible to send the paper.

Maximum numbers of authors in a paper is limited to three.

Paper Template: IEEE Format limited to three pages.

Important Dates:

Particulars	Date
Last date of paper submission	20/04/2019
Paper Acceptance Intimation	28/04/2019
Last date for Registration	01/05/2019
Paper presentation contest	03/05/2019

Contact Persons (for submission of papers):

Paper should be sent any one of the following e-mail address both in the Microsoft word and PDF format.

Prof. Anilesh Dey

Mob No: 9830709269

Email ID: anilesh.dey@nit.ac.in

Prof. Surajit Bari

Mob No:9432128450/8240925284

Email ID: surajit.bari@nit.ac.in

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